Amendments to the Specification

Please replace the paragraph at page 23, line 15, with the following rewritten paragraph:

For one embodiment, address bit A_0 is also used to generate the ALT signal on line 855 and 1118 that is applied as an input to XOR gates 810, 812, 1108, and 1110. For one embodiment, the address rate on lines 853 and 1118 is the same rate as the date_a rate on lines 818. The address bit A_0 toggles at the maximum rate of the addresses and thus functions also as the ALT signal for the reformatting circuitry.